

# Streaming Authenticated Data Structures

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**Abstract.** We consider the problem of streaming verifiable computation, where both a verifier and a prover observe a stream of  $n$  elements  $x_1, x_2, \dots, x_n$  and the verifier can later delegate some computation over the stream to the prover. The prover must return the output of the computation, along with a cryptographic proof to be used for verifying the correctness of the output. Due to the nature of the streaming setting, the verifier can only keep *small local state* (e.g., logarithmic) which must be updatable in a streaming manner and with *no interaction* with the prover. Such constraints make the problem particularly challenging and rule out applying existing verifiable computation schemes.

We propose *streaming authenticated data structures*, a model that enables efficient verification of *data structure queries* on a stream. Compared to previous work, we achieve an *exponential improvement* in the prover’s running time: While previous solutions have linear prover complexity (in the size of the stream), even for queries executing in sublinear time (e.g., set membership), we propose a scheme with  $O(\log M \log n)$  prover complexity, where  $n$  is the size of the stream and  $M$  is the size of the universe of elements. Our schemes support a series of expressive queries, such as (non-)membership, successor, range search and frequency queries, over an ordered universe and even in higher dimensions. The central idea of our construction is a new authentication tree, called *generalized hash tree*. We instantiate our generalized hash tree with a hash function based on lattices assumptions, showing that it enjoys suitable algebraic properties that traditional Merkle trees lack. We exploit such properties to achieve our results.

## 1 Introduction

With the growing market of cloud computing, it is crucial to construct protocols that enable the verification of computation performed by untrusted servers. For example, when searching over our remotely stored Gmail inbox, it would be desirable if the search results could be accompanied by a cryptographic proof vouching for their correctness, e.g., that no email was omitted (either deliberately or not) from the answer.

We consider verifiable computation in a *streaming* setting, where the dataset outsourced is rapidly evolving (e.g., stock quotes, network flows, sensor streams), and the verifier can only store a small state as the stream goes by (which should be efficiently updatable). Many prior verifiable computation schemes are unsuitable in the streaming setting: Some schemes require that the verifier (client) has access to all the data ahead of time, and performs some preprocessing (e.g., [21, 44]) before outsourcing it to the prover (server). Other existing schemes allow a client to update the dataset through an interactive protocol between the client and the server (e.g., [39]). Particularly, since the

client does not have sufficient local storage to store all the data, the client needs the server’s help to update its local state. Unfortunately, requiring an interactive protocol for every update may be too expensive in a streaming setting. For example, consider a network traffic accounting application [18], where an ISP charges a customer based on the type and duration of its network flows. To enforce that the ISP is performing the accounting correctly, the ISP logs a customer’s network flows such that the customer can later make queries to the logs to perform auditing (typically the customer does not have sufficient local storage to log all the flows). In such high link-speed settings, performing an interactive protocol with every packet or flow sent is very expensive.

**Our contributions.** We introduce *streaming authenticated data structures*, and design expressive and efficient schemes that *do not require any interaction between the client and server while the stream is observed*. In our scenario, streaming elements  $x_1, x_2, \dots, x_n$  are inserted into a data structure that is stored by the prover and the verifier stores and updates small local state of size  $O(\log n)$ . At any point of time, the verifier can send a data structure query to the prover, e.g., “return the predecessor of  $y$ ” or “return the elements in the range  $[a, b]$ ”. Subsequently the prover can compute an answer and a proof in time  $O(\log M \log n)$ , where  $n$  is the size of the stream and  $M$  is the size of the ordered universe from where the elements are drawn. Our protocols are based on the difficulty of solving the *small integer solution problem* [34], and are the first ones to achieve the following properties simultaneously:

1. **Independence of prover and verifier.** While elements  $x_1, x_2, \dots, x_n$  are streaming, the verifier and the prover update their states *independently* and with *no interaction* (there is not even unidirectional communication<sup>5</sup>). The only eligible interaction occurs in the querying phase (which is inherent anyways). Such interaction consists only of one round as opposed to existing schemes in the statistical setting that can have up to a logarithmic number of interactions (e.g., [15, 16]).
2. **Efficiency.** The running times of the verifier and the prover (for both updates and queries) as well as the proof size are all logarithmic in  $n$  and  $M$ , where  $n$  is the size of the stream and  $M$  is the size of the elements universe. In comparison, existing schemes in this setting (e.g., [13, 15, 16]) incur a linear proof generation overhead on the server, even for sublinear computations (see Section 1.1). We thus achieve an exponential improvement for many common queries in the prover’s running time.
3. **Expressiveness.** Our construction supports a wide range of queries over an ordered universe, such as (non-)membership, successor, range search and frequencies. Our results can also be extended for  $d$ -dimensional elements, by applying well-known techniques from authenticated data structures [31]. To the best of our knowledge, our construction is the first streaming verifiable protocol to support such an extensive suite of queries with logarithmic prover *and* verifier complexity.

## 1.1 Related work

The research community has introduced *streaming verifiable computation*, both in the statistical [15, 16] and the cryptographic setting [13, 46], where a verifier and a prover observe a stream of  $n$  elements  $x_1, x_2, \dots, x_n$  and the verifier can delegate some computation over the stream to the prover. The protocols in [15, 16] are probabilistic and

<sup>5</sup> Such a property is not achieved in the recent work of Schröder and Schröder [46].

use multiple interactions for verification, which reveal the secret randomness. Thus they support one-shot computation tasks, whereas we allow any number of queries.

Although most of the existing streaming verifiable protocols [13, 15, 16] are particularly efficient in terms of verifier complexity (e.g., (poly-)logarithmic in the size of the stream), the main shortcoming of all previous work (except for the work of Schröder and Schröder [46], see next paragraph) is the fact that the prover complexity is *linear* in the size of the stream, *even for sublinear (logarithmic) computations*, e.g., membership and range search queries on a stream of elements drawn from an ordered universe.<sup>6</sup> This significantly limits the applicability of these protocols since such an overhead introduces a large amount of latency, making them impractical for real-world deployment. Indeed, as Cormode *et al.* [16] point out in their experimental results, “*the chief bottleneck of these protocols seems to be  $\mathcal{P}$ ’s time to make the proof*”. In this paper, we address the prover’s complexity bottleneck for queries of practical importance (e.g., range search) and we design constructions supporting logarithmic prover *and* verifier complexity. Apart from a major theoretical improvement, we believe our protocols comprise a significant step towards practical verifiable streaming protocols.

The only efficient verifiable streaming protocol (with logarithmic prover complexity) was recently introduced by Schröder and Schröder [46]. Their construction can be applied only to *sequential* streams and hence it does not support data structures like dictionaries, where the relative order is decided depending on the element that is being streamed. Therefore it cannot be used to verify range queries efficiently. Moreover, there is unidirectional communication from the verifier to the prover per stream update.

Practical streaming verifiable computation has been also studied by the database community, with often increased worst-case complexities. Li *et al.* [29] considered verifying queries on a data stream with sliding windows, hence the verifier’s space is proportional to the window size. The protocol of Papadopoulos *et al.* [37] verifies continuous queries over a stream, again requiring linear verifier space in the worst case.

Other related works such as *verifiable computation* [2, 8, 19–22, 38, 44] and *authenticated data structures* [17, 24, 25, 35, 39–42] are not directly applicable to the streaming setting or their application yields high complexities or interactive protocols.

**Why common solutions fail.** Traditional Merkle trees [32] (using collision resistant hash functions like SHA-2) can be used to provide very efficient proofs for membership and range search queries in logarithmic time. However, since the client cannot keep linear amount of local state, in order to update the digest when a new item is streamed, the client needs to interact with the server, where the server returns and proves the correctness of the path of the Merkle tree that is “touched” by the update (e.g., see [39]).

To avoid interaction with the prover, one could use accumulator-based solutions (e.g., [12, 36]) or constructions that have low incrementality cost (e.g., [5–7, 14]). Indeed, such solutions have the attractive property that a set of elements can be represented with a small digest that can be updated in a very straightforward way, e.g., by performing an exponentiation and with no interaction. However, this property comes at some significant cost, since proofs for verification can be computed in linear time (or time  $O(n^\epsilon)$ , e.g., see [40]) which translates into increased prover complexity.

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<sup>6</sup> For linear-time computations, linear complexity at the prover is acceptable, e.g., see [15].

Our work combines the merits of the above paradigms, enabling flexible updates with no interaction and at the same time achieving logarithmic prover complexity.

## 1.2 Our techniques

The core idea of our scheme is a new primitive called *generalized hash tree*, which is a generalization of traditional Merkle trees [32]. Generalized hash trees can be instantiated with various collision resistant hash functions. In our construction we choose the hash function  $h_n(\mathbf{x}, \mathbf{y}) = \mathbf{L}\mathbf{x} + \mathbf{R}\mathbf{y} \bmod q$  (originally introduced in [1]), where  $\mathbf{L}, \mathbf{R}$  are picked at random from  $\mathbb{Z}_q^{k \times m}$  and  $\mathbf{x}, \mathbf{y}$  are  $m$ -dimensional vectors with entries bounded by  $n < q$ . As in Merkle trees, we hierarchically apply this hash function over a binary tree—however this creates a problem, since the output of the above hash function is a vector of different dimensions and larger entries than its inputs. To overcome this problem, we devise a way to map the outputs back to the input domain. Although many mappings could be used, we choose one that maintains the function’s homomorphic properties (Figure 4.2), allowing us to express the label of the root (i.e., the root hash) as the sum of well-defined functions of the leaves called *partial labels* (see Definition 16).

For example if the stream contains elements  $\{3, 4, 6, 7\}$ , we can simply express the label of the root of our hash tree as  $\mathcal{L}(3) + \mathcal{L}(4) + \mathcal{L}(6) + \mathcal{L}(7)$ , where  $\mathcal{L}(x)$  is the partial label that depends only on  $x$  and on the public matrices  $\mathbf{L}$  and  $\mathbf{R}$ , and which can be computed in logarithmic time. Clearly, such representation allows for efficient streaming updates of the verifier’s state (label of the root), just like accumulators constructions [12]. More importantly (and unlike accumulators constructions), a proof for any element can still be computed in logarithmic time, by having the prover maintain an appropriate Merkle-tree-like authenticated data structure.

## 2 Definitions

We now present definitions for streaming authenticated data structures (SADS<sup>7</sup>). Our definitions are similar to the ones given by Chung *et al.* [13] for streaming delegation, adjusted to the data structures setting. We denote with  $k$  the security parameter and with  $n = \text{poly}(k)$  an upper bound on the size of the stream.<sup>8</sup> PPT stands for *probabilistic polynomial-time* and  $\text{neg}(k)$  is a negligible function, i.e., a function less than  $1/p(k)$ , for all polynomials  $p(k)$ . Finally we define  $[n] = \{0, 1, \dots, n\}$ .

**Definition 1 (SADS scheme).** *Let  $D$  be any data structure that supports queries  $q$  and updates  $\text{upd}$ . An SADS (streaming authenticated data structure) scheme  $\mathcal{A}$  is a collection of the following six PPT algorithms:*

1.  $\text{pk} \leftarrow \text{genkey}(1^k, n)$ : *On input the security parameter  $k$  and an upper bound  $n$  on the size of the stream, it outputs a public key  $\text{pk}$ ;*
2.  $\{\text{auth}(D_0), d_0\} \leftarrow \text{initialize}(D_0, \text{pk})$ : *On input an empty data structure  $D_0$  and the public key  $\text{pk}$ , it computes the authenticated data structure  $\text{auth}(D_0)$  and the respective state  $d_0$  of it;*

<sup>7</sup> This acronym has also been used by Pappas *et al.* [43] to denote a private search system.

<sup>8</sup> Otherwise (i.e., if  $n$  is not  $\text{poly}(k)$ ) the server might need exponential space.

3.  $d_{h+1} \leftarrow \text{updateVerifier}(\text{upd}, d_h, \text{pk})$ : On input an update  $\text{upd}$  to data structure  $D_h$ , the current state  $d_h$  and the public key  $\text{pk}$ , it outputs the updated state  $d_{h+1}$  (run by verifier);
4.  $\{D_{h+1}, \text{auth}(D_{h+1})\} \leftarrow \text{updateProver}(\text{upd}, D_h, \text{auth}(D_h), \text{pk})$ : On input an update  $\text{upd}$  to data structure  $D_h$ , the authenticated data structure  $\text{auth}(D_h)$  and the public key  $\text{pk}$ , it outputs the updated data structure  $D_{h+1}$  along with the updated authenticated data structure  $\text{auth}(D_{h+1})$  (run by prover);
5.  $\{\alpha(q), \Pi(q)\} \leftarrow \text{query}(q, D_h, \text{auth}(D_h), \text{pk})$ : On input a query  $q$  on data structure  $D_h$ , the authenticated data structure  $\text{auth}(D_h)$  and the public key  $\text{pk}$ , it returns the answer  $\alpha(q)$  to the query, along with a proof  $\Pi(q)$  (run by prover);
6.  $\{1, 0\} \leftarrow \text{verify}(q, \alpha(q), \Pi(q), d_h, \text{pk})$ : On input a query  $q$ , an answer  $\alpha(q)$ , a proof  $\Pi(q)$  for query  $q$ , a digest  $d_h$  and the public key  $\text{pk}$ , it outputs either 1 (accepts) or 0 (rejects) (run by verifier);

As part of the data structure specification (and not of the above definition), we also define the algorithm  $\{0, 1\} \leftarrow \text{check}(q, \alpha, D_h)$  such that it outputs 1 if and only if  $\alpha$  is the correct answer to query  $q$  on data structure  $D_h$  (otherwise it outputs 0).

Note that there is *no secret key in our definition*, supporting in this way a stronger definition with *public verifiability*, as opposed to other verifiable streaming constructions that appear in the literature [13, 46], where the verifier's state needs to be secret.

There are two properties that an SADS scheme should satisfy, namely *correctness* and *security* (as in signature schemes definitions).

**Definition 2 (Correctness).** Let  $\mathcal{A}$  be an SADS scheme consisting of the set of algorithms  $\{\text{genkey}, \text{initialize}, \text{updateVerifier}, \text{updateProver}, \text{query}, \text{verify}\}$ . We say that the SADS scheme  $\mathcal{A}$  is correct if, for all  $k \in \mathbb{N}$ , for all  $\text{pk}$  output by algorithm  $\text{genkey}$ , for all  $D_h, \text{auth}(D_h), d_h$  output by one invocation of  $\text{initialize}$  followed by polynomially-many invocations of  $\text{updateVerifier}$  and  $\text{updateProver}$ , where  $h \geq 0$ , for all queries  $q$  and for all  $\Pi(q), \alpha(q)$  output by  $\text{query}(q, D_h, \text{auth}(D_h), \text{pk})$ , with all but negligible probability  $\text{neg}(k)$ , it holds that  $1 \leftarrow \text{verify}(q, \Pi(q), \alpha(q), d_h, \text{pk})$ .

**Definition 3 (Security).** Let  $\mathcal{A}$  be an SADS scheme consisting of the set of algorithms  $\{\text{genkey}, \text{initialize}, \text{updateVerifier}, \text{updateProver}, \text{query}, \text{verify}\}$ ,  $k$  be the security parameter,  $D_0$  be the empty data structure and  $\text{pk} \leftarrow \text{genkey}(1^k)$ . Let also  $\text{Adv}$  be a PPT adversary and let  $d_0$  be the state output by  $\text{initialize}(D_0, \text{pk})$ .

- (Update) For  $i = 0, \dots, h-1 = \text{poly}(k)$ ,  $\text{Adv}$  picks the update  $\text{upd}_i$  to data structure  $D_i$ . Let  $d_{i+1} \leftarrow \text{updateVerifier}(\text{upd}_i, d_i, \text{pk})$  be the new state corresponding to the updated data structure  $D_{i+1}$ .
- (Forge)  $\text{Adv}$  outputs a query  $q$ , an answer  $\alpha$  and a proof  $\Pi$ .

We say that the SADS scheme  $\mathcal{A}$  is secure if for all  $k \in \mathbb{N}$ , for all  $\text{pk}$  output by algorithm  $\text{genkey}$ , and for any PPT adversary  $\text{Adv}$  it holds that

$$\Pr \left[ \begin{array}{l} \{q, \Pi, \alpha\} \leftarrow \text{Adv}(1^k, \text{pk}); 1 \leftarrow \text{verify}(q, \alpha, \Pi, d_h, \text{pk}); \\ 0 \leftarrow \text{check}(q, \alpha, D_h). \end{array} \right] \leq \text{neg}(k). \quad (2.1)$$

### 3 Small integer solution problem

The security of our constructions is based on the hardness of the *small integer solution* problem, as given in the following definition:

**Definition 4 (Problem  $\text{SIS}_{q,\mu,\beta}$ ).** Given an integer  $q$ , a matrix  $\mathbf{M} \in \mathbb{Z}_q^{k \times \mu}$  picked uniformly at random (where  $\mu \geq k$ ) and a real  $\beta$ , find an integer vector  $\mathbf{z} \in \mathbb{Z}^\mu \setminus \{\mathbf{0}\}$  such that  $\mathbf{M}\mathbf{z} = \mathbf{0} \pmod{q}$  and  $\|\mathbf{z}\| \leq \beta$ .

For certain parameters, Micciancio and Peikert [33] proved that  $\text{SIVP}_\gamma$  (shortest independent vector problem [45]), a hard problem in lattices, reduces to  $\text{SIS}_{q,\mu,\beta}$  for  $\gamma = \text{poly}(k)$ . In the following we state an immediate corollary of Theorem 1.1 in [33]:

**Corollary 1 (Reducing  $\text{SIVP}_\gamma$  to  $\text{SIS}_{q,\mu,\beta}$  [33]).** Let  $\text{SIS}_{q,\mu,\beta}$  be an instance of the small integer solution problem. Let also  $\beta, \mu, q$  be  $\text{poly}(k)$ , where  $q$  is a prime such that  $q \geq \beta \cdot k^\delta$  for some  $\delta > 0$ .  $\text{SIS}_{q,\mu,\beta}$  is as hard as approximating the problem  $\text{SIVP}_\gamma$  in the worst case to within certain  $\gamma = \frac{\beta}{k^\delta} \cdot O(\beta\sqrt{k} \cdot \text{poly}(\log k))$ .

For exponential values of  $\gamma$ , i.e.,  $\gamma = 2^{O(k)}$ , one can use the LLL algorithm [28] and solve the  $\text{SIVP}_\gamma$  problem in polynomial time. However, for polynomial  $\gamma$ , no efficient algorithm is known to date, even for factors slightly smaller than exponential [45]. Therefore, for the parameters of Corollary 1,  $\text{SIS}_{q,\mu,\beta}$  is also hard, leading to the following assumption:

**Assumption 1 (Hardness of  $\text{SIS}_{q,\mu,\beta}$ ).** Let  $k$  be the security parameter and  $\text{SIS}_{q,\mu,\beta}$  be an instance of the small integer solution problem. Let also  $\beta, \mu, q$  be  $\text{poly}(k)$ , where  $q$  is a prime such that  $q \geq \beta \cdot k^\delta$  for some  $\delta > 0$ . There is no PPT algorithm for solving  $\text{SIS}_{q,\mu,\beta}$ , except with negligible probability  $\text{neg}(k)$ .

### 3.1 Setting the parameters $q, \mu$ and $\beta$

For the application we are considering in this paper, we are using an instance of the problem  $\text{SIS}_{q,\mu,\beta}$  where  $\beta$  (i.e., the norm of the solution vector) takes polynomially-large values depending on a polynomially-bounded application parameter  $n$  ( $n$  will be the size of the stream). Specifically we are going to use the parameters  $q, \mu, \beta$  as set by the algorithm  $\text{parameters}(1^k, n)$  in Figure 3.1.

We note here that the parameters in Figure 3.1 comply with Corollary 1: First, as  $n = \text{poly}(k)$ , all  $q, \mu, \beta$  are  $\text{poly}(k)$ . Second,  $q/\sqrt{\lceil \log q \rceil} \geq \sqrt{2} \cdot n \cdot k^{0.5+\delta} \Leftrightarrow q \geq \beta \cdot k^\delta$ , since  $\beta = n\sqrt{\mu}$  and  $\mu = 2k\lceil \log q \rceil$ . Also note that there is always a prime  $q = \Theta(n \cdot k^{0.5+\delta} \cdot \sqrt{\log k})$  satisfying the inequality above, for some  $\delta > 0$ .

### 3.2 The hash function

Our construction uses a hash function that is a syntactic modification (it accepts two inputs instead of one) of the collision resistant hash function presented by Micciancio and Regev [34], following seminal work by Ajtai [1] and Goldreich *et al.* [23]. The security of our function is based on the hardness of  $\text{SIS}_{q,\mu,\beta}$ , using the parameters by Micciancio and Peikert [33], as shown above. We note here that a similar two-input hash function was also used to build a string commitment scheme by Kawachi *et al.* [27].

**Definition 5 (Hash function [33, 34]).** Let  $k$  be the security parameter,  $n = \text{poly}(k)$  and  $q, \mu, \beta$  be the parameters output by algorithm  $\text{parameters}(1^k, n)$ . Set  $m = \frac{\mu}{2}$ . Let also  $\mathbf{L}, \mathbf{R} \in \mathbb{Z}_q^{k \times m}$  be two  $k \times m$  matrices picked uniformly at random. We define the function  $h_n : [n]^m \times [n]^m \rightarrow \mathbb{Z}_q^k$  as  $h_n(\mathbf{x}, \mathbf{y}) = \mathbf{L} \cdot \mathbf{x} + \mathbf{R} \cdot \mathbf{y} \pmod{q}$ .

**Algorithm**  $\{q, \mu, \beta\} \leftarrow \text{parameters}(1^k, n)$ : For  $n = \text{poly}(k)$ , let  $q$  be the smallest prime satisfying  $q/\sqrt{\lceil \log q \rceil} \geq \sqrt{2} \cdot n \cdot k^{0.5+\delta}$  for some  $\delta > 0$ . Set  $\mu = 2k \lceil \log q \rceil$  and  $\beta = n\sqrt{\mu}$ .

**Fig. 3.1.** Setting the parameters of  $\text{SIS}_{q,\mu,\beta}$  as a function of the application parameter  $n$ .

**Theorem 1 (Collision resistance [33, 34]).** *Let  $k$  be the security parameter,  $n = \text{poly}(k)$  and  $\{q, \mu, \beta\} \leftarrow \text{parameters}(1^k, n)$ . Set  $m = \frac{\mu}{2}$ . Let also  $\mathbf{L}, \mathbf{R} \in \mathbb{Z}_q^{k \times m}$  be matrices picked uniformly at random. Assuming hardness of  $\text{SIS}_{q,\mu,\beta}$  (see Assumption 1), there is no PPT algorithm that outputs two distinct pairs of vectors  $(\mathbf{x}_1, \mathbf{y}_1) \in [n]^m \times [n]^m$  and  $(\mathbf{x}_2, \mathbf{y}_2) \in [n]^m \times [n]^m$  such that  $\mathbf{L} \cdot \mathbf{x}_1 + \mathbf{R} \cdot \mathbf{y}_1 = \mathbf{L} \cdot \mathbf{x}_2 + \mathbf{R} \cdot \mathbf{y}_2 \pmod q$ , except with negligible probability  $\text{neg}(k)$ .*

### 3.3 Binary representations

For our constructions, we are going to need *binary representations* of vectors:

**Definition 6 (Binary representation of scalars).** *Let  $\tau = \lceil \log q \rceil$ . Denote with  $\mathbf{b}(a) = [\mathbf{b}_0, \mathbf{b}_1, \dots, \mathbf{b}_{\tau-1}]^T \in \{0, 1\}^\tau$  the binary representation of  $a \in \mathbb{Z}_q$ , i.e.,  $a = \sum_{i=0}^{\tau-1} \mathbf{b}_i 2^i$ .*

Note now that Definition 6 can be naturally extended for vectors  $\mathbf{a} \in \mathbb{Z}_q^k$ : For  $i = 0, \dots, k-1$ ,  $\mathbf{a}_i$  is mapped to the respective  $\tau$  entries  $\mathbf{b}(\mathbf{a}_i)$  in the resulting vector  $\mathbf{b}(\mathbf{a})$ :

**Definition 7 (Binary representation of vectors).** *Let  $\mathbf{a} = [\mathbf{a}_0, \mathbf{a}_1, \dots, \mathbf{a}_{k-1}]^T \in \mathbb{Z}_q^k$ . We denote with  $\mathbf{b}(\mathbf{a}) = [\mathbf{b}(\mathbf{a}_0), \mathbf{b}(\mathbf{a}_1), \dots, \mathbf{b}(\mathbf{a}_{k-1})]^T \in \{0, 1\}^{k \cdot \tau}$  ( $\tau = \lceil \log q \rceil$ ) the binary representation of  $\mathbf{a} \in \mathbb{Z}_q^k$ , where  $\mathbf{b}(\mathbf{a}_i)$  is defined in Definition 6.*

For example, if  $k = 2$ ,  $q = 8$  and  $\mathbf{a} = [6, 3]^T \in \mathbb{Z}_8^2$ , then  $\mathbf{b}(\mathbf{a}) = [0, 1, 1, 1, 1, 0]^T$ , since  $\mathbf{b}(6) = [0, 1, 1]^T$  and  $\mathbf{b}(3) = [1, 1, 0]^T$ .

## 4 Generalized hash trees

The main primitive of our construction is what we call a *generalized hash tree*. A generalized hash tree has several differences from the traditional Merkle hash tree [32].

First we recall that a Merkle hash tree is a labeled binary tree  $T$  where the label  $\lambda(w)$  of every node  $w$  is the collision resistant hash (e.g., a SHA-2 hash) of the labels  $\lambda(u)$  and  $\lambda(v)$  and of its children  $u$  and  $v$ , i.e.,  $\lambda(w) = h(\lambda(u), \lambda(v))$ . When function  $h$  is applied recursively on all the nodes of the tree, the label  $\lambda(r)$  of the root  $r$  has the following property: A PPT adversary cannot find two different data sets at the leaves that produce the same label at the root of a Merkle tree.

In our work, instead of using a hash function such as SHA-2 that lacks algebraic structure, we employ the hash function  $h_n$  described in Section 3. However, we cannot directly apply this function since its domain (vectors in  $[n]^m$ ) is different from its range (vectors in  $\mathbb{Z}_q^k$ ). Generalized hash trees, introduced in the next section, provide a way to overcome this domain-range discrepancy problem.

## 4.1 Defining generalized hash trees

Let  $h : \mathcal{D} \times \mathcal{D} \rightarrow \mathcal{R}$  be a collision resistant hash function accepting two inputs that take values from domain  $\mathcal{D}$  and outputting a value in a *different* range  $\mathcal{R}$ . Generalized hash trees solve the domain-range discrepancy problem (and at the same time maintain the authentication and algorithmic properties of traditional Merkle hash trees [32]) as follows: They require that the labels  $\lambda(u) \in \mathcal{D}$  and  $\lambda(v) \in \mathcal{D}$  of the children  $u$  and  $v$  hash to a *deterministic* and *easily computable* projection function  $f : \mathcal{D} \rightarrow \mathcal{R}$  of the label  $\lambda(w) \in \mathcal{D}$  of the parent  $w$ , i.e.,  $f(\lambda(w)) = h(\lambda(u), \lambda(v))$ .

An immediate implication of this property is that the labels of a generalized hash tree are generally *not* uniquely determined by the labels of the leaves: In the above example,  $\lambda(w)$  can be any  $f$ -preimage of  $h(\lambda(u), \lambda(v))$ . However, the collision resistant property of Merkle trees is still true: Any two valid hash trees representing different data sets at the leaves but with the same root label yield a collision to the underlying hash function. We now continue with defining generalized hash trees formally. We first need the following definition for representing binary trees.

**Definition 8 (Full binary tree).** A full binary tree  $T$  is a non-empty tree where every internal node has two children. It is represented with set of binary strings, where  $\epsilon$  is the empty string representing the root of  $T$  and  $w0$  and  $w1$  are the string representations of the left and right children of a node having string representation  $w$ .

For example, a full binary tree with five nodes is  $T = \{\epsilon, 0, 1, 00, 01\}$ . Note that full binary trees need not be complete, i.e., not all leaves must lie at the same level.

**Definition 9 (Labeled binary tree).** A labeled binary tree  $(T, \lambda)$  is a full binary tree  $T$  along with labels  $\lambda(w)$  for all  $w \in T$ .

**Definition 10 (Generalized hash tree).** Given functions  $h : \mathcal{D} \times \mathcal{D} \rightarrow \mathcal{R}$  and  $f : \mathcal{D} \rightarrow \mathcal{R}$ , a generalized hash tree  $(T, \lambda, f, h)$  is a labeled binary tree  $(T, \lambda)$  such that **(a)** for all  $w \in T$ ,  $\lambda(w) \in \mathcal{D}$ ; **(b)** for all internal nodes  $w \in T$ ,  $f(\lambda(w)) = h(\lambda(w0), \lambda(w1))$ , where  $w0$  and  $w1$  are the left and right children of  $w$  respectively.

**Definition 11 (Tree collision).** A tree collision is a pair of two distinct generalized hash trees  $(T, \lambda, f, h)$  and  $(T, l, f, h)$  such that  $\lambda(\epsilon) = l(\epsilon)$ .

We now give our main security theorem, establishing collision resistance for generalized hash trees. The proof is in the Appendix (see Section 7.1).

**Theorem 2 (Collision resistance).** Let  $k$  be the security parameter,  $T$  be a full binary tree of  $\text{poly}(k)$  depth. If  $h$  is collision resistant, there is no PPT algorithm that can output a tree collision  $(T, \lambda, f, h)$  and  $(T, l, f, h)$ , except with probability  $\text{neg}(k)$ .

## 4.2 An instantiation of generalized hash trees

In our application setting, we are using a *structured binary tree* which is a special case of the full binary tree from Definition 8:

**Definition 12 (Structured binary tree).** Let  $M$  be a power of two. A structured binary tree  $T_C$  is a full binary tree  $T$  of  $\log M$  levels where all the leaves lie at the last level of the tree, storing values  $\mathcal{C} = [c_0, c_1, \dots, c_{M-1}]$ , where  $c_i \in \mathbb{Z}_q$ .

**Function  $\mathbf{y} = f(\mathbf{x})$ :** Let  $\tau = \lceil \log q \rceil$ . On input a vector  $\mathbf{x} \in [n]^m$ , where  $m = k \cdot \tau$ , output a vector  $\mathbf{y}$  of  $k$  entries such that each  $\mathbf{y}_i$  ( $i = 0, \dots, k - 1$ ) is the number in  $\mathbb{Z}_q$  represented by the radix-2 representation  $[\mathbf{x}_{i\tau}, \mathbf{x}_{i\tau+1}, \dots, \mathbf{x}_{(i+1)\tau-1}]^\top$ , namely

$$\mathbf{y}_i = \sum_{j=0}^{\tau-1} \mathbf{x}_{i\tau+j} 2^j \pmod q, \text{ for } i = 0, \dots, k - 1.$$

**Fig. 4.2.** The projection function  $f$ . It parses the input  $\mathbf{x}$  as a vector of radix-2 representations and converts it to a vector  $\mathbf{y}$  (of smaller dimension) storing the respective numbers in  $\mathbb{Z}_q$ .

**Definition 13 (Range of a node).** Let  $w$  be a node of a structured binary tree  $T_C$ . The set  $\text{range}(w)$  contains the leaves of the subtree of  $T_C$  rooted on  $w$ .

In the following sections, we instantiate the generalized hash tree for a structured binary tree using the lattice-based hash function  $h_n(\mathbf{x}, \mathbf{y}) = \mathbf{L} \cdot \mathbf{x} + \mathbf{R} \cdot \mathbf{y}$  from Definition 5, where  $\mathcal{D} = [n]^m$  and  $\mathcal{R} = \mathbb{Z}_q^k$ —see Section 3 for the definition of all parameters  $k, n, m, q$ . We will also show which projection function  $f$  to use and how to compute the labels  $\lambda$  so that Definition 10 is satisfied.

### 4.3 The projection function $f$

The projection function  $f : [n]^m \rightarrow \mathbb{Z}_q^k$  we use is very simple. It *parses* the input vector  $\mathbf{x}$  as a radix-2 representation (i.e., a base-2 representation but not necessarily of binary coefficients) and converts it to the respective vector in  $\mathbb{Z}_q^k$ . We give the code of the function in Figure 4.2. We now have the following corollary for function  $f$ :

**Corollary 2 (Applying function  $f$  to binary representations).** Let  $\mathbf{a} \in \mathbb{Z}_q^k$ . Then  $f(\mathbf{b}(\mathbf{a})) = \mathbf{a}$ , where  $\mathbf{b}(\mathbf{a})$  is the binary representation of  $\mathbf{a}$  defined in Definition 6.

Clearly, function  $f$  is a linear function. This property (stated below) is crucial for proving that the labels (defined in Section 4.4) comply with Definition 10:

**Corollary 3 (Linearity of function  $f$ ).** Let  $\mathbf{x} \in [n]^m$  and  $\mathbf{y} \in [n]^m$  such that  $\mathbf{x} + \mathbf{y} \in [n]^m$ . Then  $f(\mathbf{x} + \mathbf{y}) = f(\mathbf{x}) + f(\mathbf{y})$ .

### 4.4 Computing the labels

We now continue with defining the labels of the generalized hash tree (see Definition 16). Before that, we give some necessary definitions:

**Definition 14.** Define the functions  $g_0 : [n]^m \rightarrow [n]^m$  and  $g_1 : [n]^m \rightarrow [n]^m$  such that  $g_0(\mathbf{x}) = \mathbf{b}(\mathbf{L} \cdot \mathbf{x})$  and  $g_1(\mathbf{x}) = \mathbf{b}(\mathbf{R} \cdot \mathbf{x})$ . Also, for a bitstring  $w = b_1 b_2 \dots b_e$ , define the function  $g_w : [n]^m \rightarrow [n]^m$  as the composition  $g_w(\mathbf{x}) = g_{b_1} \circ g_{b_2} \circ \dots \circ g_{b_e}(\mathbf{x})$ .

**Definition 15 (Partial labels of a node  $w$ ).** Let  $T_C$  be a structured binary tree. The partial label of a leaf node  $v$  with respect to itself is defined as  $\mathcal{L}_v(v) = \mathbf{1}$ , where  $\mathbf{1} = [1, 1, \dots, 1]^\top \in [n]^m$ . For every other node  $w$  of  $T_C$ , and for every leaf  $v \in \text{range}(w)$ , the partial label  $\mathcal{L}_w(v)$  of  $w$  with respect to  $v$  is defined as  $\mathcal{L}_w(v) = g_{v-w}(\mathbf{1})$ , where  $v - w$  is the result of removing prefix  $w$  from bitstring  $v$ .

E.g., for a structured binary tree of 8 leaves, the partial label of the root wrt leaves 2 and 3 are  $\mathcal{L}_\epsilon(2) = \mathbf{b}(\mathbf{L} \cdot \mathbf{b}(\mathbf{R} \cdot \mathbf{b}(\mathbf{L} \cdot \mathbf{1})))$  and  $\mathcal{L}_\epsilon(3) = \mathbf{b}(\mathbf{L} \cdot \mathbf{b}(\mathbf{R} \cdot \mathbf{b}(\mathbf{R} \cdot \mathbf{1})))$  respectively.

**Definition 16.** Let  $T_{\mathcal{C}}$  be a structured binary tree, where  $\mathcal{C} = [c_0, c_1, \dots, c_{M-1}]$ . For every node  $w \in T_{\mathcal{C}}$  we define a function  $\lambda(w) = \sum_{v \in \text{range}(w)} c_v \cdot \mathcal{L}_w(v)$ .

**Lemma 1.** Let  $T_{\mathcal{C}}$  be a structured binary tree. If  $\sum_{i=0}^{M-1} c_i \leq n$ , then for all nodes  $w \in T_{\mathcal{C}}$  it holds that  $\lambda(w) \in [n]^m$ , where  $\lambda(w)$  is the function defined in Definition 16.

*Proof.* Write  $\lambda(w)$  as in Definition 16. Since  $\sum_{i=0}^{M-1} c_i \leq n$  and the entries of each partial label  $\mathcal{L}_w(v)$  are in  $\{0, 1\}$ , it follows that  $\lambda(w) \in [n]^m$ .  $\square$

**Lemma 2.** Let  $T_{\mathcal{C}}$  be a structured binary tree. If  $\sum_{i=0}^{M-1} c_i \leq n$ , then  $f(\lambda(w)) = \mathbf{L} \cdot \lambda(w0) + \mathbf{R} \cdot \lambda(w1)$ , where  $\lambda(w)$  is the function defined in Definition 16 and  $w$  is any internal node of  $T_{\mathcal{C}}$ .

*Proof.* Let  $w$  be an internal node of the structured binary tree  $T_{\mathcal{C}}$ . Let  $w0$  be its left child and  $w1$  be its right child. Since  $\sum c_i \leq n$ , by Lemma 1, it is  $\lambda(w) \in [n]^m$ , so we can apply function  $f$ . Therefore we have

$$\begin{aligned}
f(\lambda(w)) &= f\left(\sum_{v \in \text{range}(w)} c_v \cdot \mathcal{L}_w(v)\right) \quad (\text{Def. 16}) \\
&= \sum_{v \in \text{range}(w0)} c_v \cdot f(\mathcal{L}_w(v)) + \sum_{v \in \text{range}(w1)} c_v \cdot f(\mathcal{L}_w(v)) \quad (\text{Cor. 3}) \\
&= \sum_{v \in \text{range}(w0)} c_v \cdot f(g_{w-v}(\mathbf{1})) + \sum_{v \in \text{range}(w1)} c_v \cdot f(g_{w-v}(\mathbf{1})) \quad (\text{Def. 15}) \\
&= \sum_{v \in \text{range}(w0)} c_v \cdot f(g_0(g_{w0-v}(\mathbf{1}))) + \sum_{v \in \text{range}(w1)} c_v \cdot f(g_1(g_{w1-v}(\mathbf{1}))) \quad (\text{Def. 14}) \\
&= \sum_{v \in \text{range}(w0)} c_v \cdot f(g_0(\mathcal{L}_{w0}(v))) + \sum_{v \in \text{range}(w1)} c_v \cdot f(g_1(\mathcal{L}_{w1}(v))) \quad (\text{Def. 15}) \\
&= \sum_{v \in \text{range}(w0)} c_v \cdot f(\mathbf{b}(\mathbf{L} \cdot \mathcal{L}_{w0}(v))) + \sum_{v \in \text{range}(w1)} c_v \cdot f(\mathbf{b}(\mathbf{R} \cdot \mathcal{L}_{w1}(v))) \quad (\text{Def. 14}) \\
&= \sum_{v \in \text{range}(w0)} c_v \cdot \mathbf{L} \cdot \mathcal{L}_{w0}(v) + \sum_{v \in \text{range}(w1)} c_v \cdot \mathbf{R} \cdot \mathcal{L}_{w1}(v) \quad (\text{Cor. 2}) \\
&= \mathbf{L} \cdot \lambda(w0) + \mathbf{R} \cdot \lambda(w1) \quad (\text{Def. 16}). \text{ This completes the proof. } \square
\end{aligned}$$

**Theorem 3.** Let  $T_{\mathcal{C}}$  be a structured binary tree. If  $\sum_{i=0}^{M-1} c_i \leq n$ , then  $(T_{\mathcal{C}}, \lambda, f, h_n)$  is a generalized hash tree, where  $h_n(\mathbf{x}, \mathbf{y}) = \mathbf{L} \cdot \mathbf{x} + \mathbf{R} \cdot \mathbf{y}$  is the function from Definition 5,  $\lambda$  is defined in Definition 16 and  $f$  is the function in Figure 4.2.

*Proof.* It follows from Lemmas 1 and 2 and by Definition 10.  $\square$

## 4.5 Efficient updates of the labels

Note that Definition 16 enables very efficient updates of the label of any node, whenever a leaf value changes. For example, if  $\lambda(\epsilon)$  is the label of the root of a generalized hash tree  $(T_{\mathcal{C}}, \lambda, f, h_n)$  with eight leaves  $\{0, 1, 2, \dots, 7\}$  where  $c_3 = 2, c_4 = c_6 = c_7 = 1$  and  $c_0 = c_1 = c_2 = c_5 = 0$ , then the root label  $\lambda(\epsilon)$  can be expressed as  $2\mathcal{L}_{\epsilon}(3) + \mathcal{L}_{\epsilon}(4) + \mathcal{L}_{\epsilon}(6) + \mathcal{L}_{\epsilon}(7)$ . Particularly, each occurrence of an element  $i$  contributes  $\mathcal{L}_{\epsilon}(i)$  (i.e., partial label of the root  $\epsilon$  with respect to  $i$ ) to the root label. Adding (or removing) an element  $x$  to the set is equivalent to adding  $\mathcal{L}_{\epsilon}(x)$  (or  $-\mathcal{L}_{\epsilon}(x)$ ) to  $\lambda(\epsilon)$ . It is also important to note that the partial labels (defined in Definition 15) required for such updates can be easily computed in polylogarithmic time:

**Lemma 3.** *The partial label  $\mathcal{L}_w(v)$  can be computed in time  $O(\log M \log^2 n)$ .*

*Proof.* Computing  $\mathcal{L}_w(v)$ , by Definition 15, requires  $O(\log M)$  recursive calls, each one of which involves: (a) computing a binary representation of  $k$   $O(\log q)$ -bit numbers, which takes time  $O(k \log q)$ ; (b) multiplying a  $k \times O(k \log q)$  matrix with a vector of  $O(k \log q)$  bits, which takes time  $O(k \log^2 q)$ . This completes the proof.  $\square$

## 5 Our SADS construction

Let  $T_{\mathcal{C}}$  be a structured binary tree with  $M$  leaves corresponding to the universe of integer values  $\mathcal{U} = \{0, 1, \dots, M - 1\}$ . For our construction, we are using a generalized hash tree  $(T_{\mathcal{C}}, \lambda, f, h_n)$  as described in the previous section, where  $\lambda$  is defined in Definition 16,  $f$  is the function in Figure 4.2,  $h_n$  is the hash function from Definition 5 and  $\{c_0, c_1, \dots, c_{M-1}\}$  correspond to the frequency of elements  $\{0, 1, \dots, M - 1\}$  appearing in the stream. Note that even for an exponential value of  $M$ , the condition  $\sum_{i=0}^{M-1} c_i \leq n$  of Theorem 3 still holds since for the elements  $x$  that do not appear in the stream it is  $c_x = 0$ . To store the generalized hash tree, we store only the labels that are defined on the paths from non-zero leaves to the root (all other labels are zero). This requires space proportional to  $O(\nu \log M)$ , where  $\nu$  is the number of distinct element appearing in the stream. In this way, we avoid storing  $O(M)$  space, which is prohibitive given the potential exponential universe size  $M$ .

Figure 5.3 presents our SADS scheme for frequency queries. We note that algorithms query and verify are the same for all generalized hash trees, unlike the update algorithms that are specific for the algebraic hash function  $h_n$ .

### 5.1 Range search queries

In this section we show how to support range search queries. The proof for a range search query  $[x, y]$  simply contains the two proofs  $\Pi(x)$  and  $\Pi(y)$  as output by algorithms  $\text{query}(x, D_h, \text{auth}(D_h), \text{pk})$  and  $\text{query}(y, D_h, \text{auth}(D_h), \text{pk})$  respectively from Figure 5.3. It also contains the frequencies  $\mathcal{C}_{xy} = \{c_{a_1}, c_{a_2}, \dots, c_{a_s}\}$  of the reported range as an answer. Let now  $\mathcal{R}_{xy} = \{a_1, a_2, \dots, a_s\}$  denote the respective reported range that corresponds to  $\mathcal{C}_{xy}$ .

For verification, the proofs  $\Pi(x)$  and  $\Pi(y)$  are verified first by using algorithm verify from Figure 5.3. If this verification is successful, perform the following test (else reject): If for all labels  $\lambda(v) \in \Pi(x) \cup \Pi(y)$  such that  $\text{range}(v) \cap \mathcal{R}_{xy}$  is not empty,

<p><b>Algorithm</b> <math>\text{pk} \leftarrow \text{genkey}(1^k, n)</math>: Call <math>\{q, \mu, \beta\} \leftarrow \text{parameters}(1^k, n)</math> from Figure 3.1, on input the security parameter <math>k</math> and a bound <math>n</math> on the size of the stream. Set <math>\text{pk} = \{\mathbf{L}, \mathbf{R}, q, \mathcal{U}\}</math>, where <math>\mathcal{U}</math> is a universe such that <math> \mathcal{U}  = M</math> and <math>\mathbf{L}, \mathbf{R}</math> are picked uniformly at random from <math>\mathbb{Z}_q^m</math> for <math>m = \frac{\mu}{2}</math>.</p>
<p><b>Algorithm</b> <math>\{\text{auth}(D_0), d_0\} \leftarrow \text{initialize}(D_0, \text{pk})</math>: Let <math>D_0</math> be a structured binary tree <math>T_C</math> where <math>c_i = 0</math> (<math>i = 0, \dots, M-1</math>). The algorithm outputs the generalized hash tree <math>(T_C, \lambda, f, h_n)</math> as <math>\text{auth}(D_0)</math>, where <math>\lambda(v) = \mathbf{0} \in [n]^m</math> for all nodes <math>v</math> in <math>T_C</math>. Also it outputs <math>d_0 = \mathbf{0} \in [n]^m</math>.</p>
<p><b>Algorithm</b> <math>d_{h+1} \leftarrow \text{updateVerifier}(x, d_h, \text{pk})</math>: Let <math>x \in \mathcal{U}</math> be the current element of the stream. The algorithm updates the local state by setting <math>d_{h+1} = d_h + \mathcal{L}_\epsilon(x)</math>, where <math>\epsilon</math> is the root of <math>T_C</math> and <math>\mathcal{L}_\epsilon(x)</math> is defined in Definition 15.</p>
<p><b>Algorithm</b> <math>\{D_{h+1}, \text{auth}(D_{h+1})\} \leftarrow \text{updateProver}(x, D_h, \text{auth}(D_h), \text{pk})</math>: Let <math>x \in \mathcal{U}</math> be the current element of the stream. The algorithm sets <math>c_x = c_x + 1</math>, outputting the updated tree <math>T_C</math>. Let <math>v_\ell, \dots, v_1</math> be the path in <math>T_C</math> from node <math>v_\ell</math> (<math>v_\ell</math> stores <math>c_x</math>) to the child <math>v_1</math> of the root <math>\epsilon</math> of <math>T_C</math>. Set</p> $\lambda(v_i) = \lambda(v_i) + \mathcal{L}_{v_i}(x) \text{ for } i = \ell, \ell-1, \dots, 1, \quad (5.2)$ <p>where <math>\mathcal{L}_{v_i}(x)</math> is defined in Definition 15. The new authenticated data structure <math>\text{auth}(D_{h+1})</math> is the new generalized hash tree with the updated labels as computed in Equation 5.2.</p>
<p><b>Algorithm</b> <math>\{\alpha(q), \Pi(q)\} \leftarrow \text{query}(q, D_h, \text{auth}(D_h), \text{pk})</math>: Let <math>q</math> be a frequency query for element <math>x \in \mathcal{U}</math>. Set <math>\alpha(q) = c_x</math> (note that if <math>c_x = 0</math>, <math>x</math> is not contained in the collection). Let <math>v_\ell, \dots, v_1</math> be the path in the structured binary tree <math>T_C</math> from node <math>v_\ell</math> (<math>v_\ell</math> stores the value <math>c_x</math>) to the child <math>v_1</math> of the root <math>\epsilon</math> of <math>T_C</math>. Let also <math>w_\ell, \dots, w_1</math> be the <i>sibling nodes</i> of <math>v_\ell, \dots, v_1</math>. Proof <math>\Pi(q)</math> contains the ordered sequence of the pairs of labels belonging to the tree path from leaf <math>v_\ell</math> to the root <math>\epsilon</math> of the tree, i.e., the pairs <math>\{(\lambda(v_\ell), \lambda(w_\ell)), (\lambda(v_{\ell-1}), \lambda(w_{\ell-1})), \dots, (\lambda(v_1), \lambda(w_1))\}</math>.</p>
<p><b>Algorithm</b> <math>\{1, 0\} \leftarrow \text{verify}(q, \alpha(q), \Pi(q), d_h, \text{pk})</math>: Let <math>q</math> be a frequency query for element <math>x \in \mathcal{U}</math>. Parse <math>\Pi(q)</math> as <math>\{(\lambda(v_\ell), \lambda(w_\ell)), \dots, (\lambda(v_1), \lambda(w_1))\}</math> and <math>\alpha(q)</math> as <math>c_x</math>. If <math>\lambda(v_\ell) \neq c_x \mathbf{1}</math> or <math>\lambda(v_\ell), \lambda(w_\ell) \notin [n]^m</math>, output 0. Compute values <math>y_{\ell-1}, y_{\ell-2}, \dots, y_0</math> as <math>y_i = \mathbf{L} \cdot \lambda(v_{i+1}) + \mathbf{R} \cdot \lambda(w_{i+1})</math> (if <math>v_{i+1}</math> is <math>v_i</math>'s left child) or <math>y_i = \mathbf{R} \cdot \lambda(v_{i+1}) + \mathbf{L} \cdot \lambda(w_{i+1})</math> (if <math>v_{i+1}</math> is <math>v_i</math>'s right child). For <math>i = \ell-1, \dots, 1</math>, if <math>f(\lambda(v_i)) \neq y_i</math> or <math>\lambda(v_i), \lambda(w_i) \notin [n]^m</math> output 0. If <math>f(d_h) \neq y_0</math>, output 0. Output 1.</p>

**Fig. 5.3.** Algorithms of the SADS scheme for verifying frequency queries.

the following relation (as in Definition 16)

$$\lambda(v) = \sum_{i \in \text{range}(v) \cap \mathcal{R}_{xy}} c_i \cdot \mathcal{L}_v(i) \quad (5.3)$$

is true, output 1 (i.e., accept), else output 0 (i.e., reject). The above relation ensures that all the range (with the correct frequencies) has been reported, or otherwise, the adversary could find a collision. The above technique can be also used for verifying successor queries, where the reported range is empty.

We now give our final result stating the formal security guarantee of our algorithms, along with their detailed asymptotic performance. The correctness of our scheme fol-

flows easily by inspecting the algorithms, therefore its proof is omitted. The security proof and the proof of asymptotic performance are in the Appendix.

**Theorem 4 (Streaming authenticated frequency with range search).** *Let  $k$  be the security parameter;  $n = \text{poly}(k)$  be an upper bound on the size of a stream containing elements from an ordered universe  $\mathcal{U}$  of size  $M$ ,  $\{q, \mu, \beta\} \leftarrow \text{parameters}(1^k, n)$  and  $\nu$  be the number of unique elements that have appeared in the stream. There exists a streaming authenticated data structure scheme for one-dimensional frequency queries and one-dimensional range queries (outputting the respective frequencies) such that: (a) It is correct according to Definition 2 and secure according to Definition 3 and assuming hardness of  $\text{SIS}_{q, \mu, \beta}$  (Assumption 1); (b) Algorithms `updateVerifier` and `updateProver` run in  $O(\log M \log^2 n)$  time; (c) Algorithm `query` (both for frequency and range search queries) runs in  $O(\log M \log n)$  time, outputting a proof of size  $O(\log M \log n)$ ; (d) A frequency query can be verified in  $O(\log M \log^2 n)$  time and a range search query can be verified in  $O(s \log M \log^2 n)$  time, where  $s$  is the size of the output range; (e) The space required at the verifier is  $O(\log n)$  and the space required at the prover is  $O(\nu \log M \log n)$ .*

Our algorithms can be extended to two (or multiple) dimensions by leveraging existing methods for multidimensional range queries [31], carefully adjusted in our framework. Due to space limitations, we defer such extensions to the full version of our paper.

## 6 Applications

In this section we present three applications of our construction.

**Cryptographic accumulator with efficient witness generation.** A cryptographic accumulator [4, 9] allows one to hash a set of inputs into one short *accumulation value*, such that there is a witness that a given input was incorporated into the accumulator, and at the same time, it is infeasible to find a witness for a value that was not accumulated. In CRYPTO 2002, Camenisch and Lysyanskaya [12] introduced *dynamic accumulators*, that enable updating the accumulation value when inputs are dynamically added or deleted, such that the cost of an update is independent of the number of accumulated inputs. However, all dynamic accumulator constructions that appeared since then (e.g., [3, 11, 12, 30, 36]) share one common limitation: Computing a witness, in absence of the trapdoor information (which has many practical applications, e.g., [40]), takes at least linear time. We observe that our construction comprises a dynamic accumulator that does not have this limitation: Specifically, for a set of elements  $\mathcal{X} \subseteq \{0, 1, \dots, M-1\}$ , our accumulation value, from Definition 16, is  $\text{acc}(\mathcal{X}) = \sum_{i \in \mathcal{X}} \mathcal{L}_\epsilon(i)$ . To update the accumulation value with element  $y$ , one has to set  $\text{acc}(\mathcal{X}) = \text{acc}(\mathcal{X}) + \gamma \cdot \mathcal{L}_\epsilon(y)$ , where  $\gamma \in \{1, -1\}$  depending on whether we add or remove  $y$  from the set. Our construction satisfies basic accumulator properties such as quasi-commutativity and efficient updates [12]. Moreover, one can use the generalized hash tree and compute witnesses in *logarithmic* time (see Theorem 4), as opposed to *linear* time.

**Parallel online memory checking in the public key setting.** Memory checking [10] studies the problem of cryptographically verifying the correctness of untrusted indexed storage by only storing small local memory. Many checkers with logarithmic *sequential* query complexity (number of reads and writes to the *untrusted* memory), e.g., [10,

35, 24], have appeared in the literature. However, *parallelizing* existing checker constructions can only be achieved in the secret key setting (e.g., see [26]). Checkers in the public key setting (e.g., [24]) cannot be naturally parallelized because they are traditionally implemented with Merkle trees [32]: Whenever a leaf value of the checker tree is written, the roothash can be updated only after the value of its child has been updated, which is an inherently sequential process. Our generalized hash tree can be used to overcome this barrier, yielding a *parallel* memory checker in the public key setting (recall we do not use any secret keys in our construction). This is because in our construction, whenever a leaf value  $i$  is written, changing its value from  $c$  to  $c'$ , we can execute algorithm `updateProver` from Section 5 in parallel (note the loop described in Relation 5.2 is fully parallelizable) and by accessing *only* the old value  $c$ , thus issuing  $O(1)$  queries to the untrusted memory. Therefore our construction yields the first parallel memory checker in the public key setting with  $O(1)$  query complexity using  $O(\log M)$  processors.

**Authenticated data structure with logarithmic space at the trusted source.** Our construction can be used for implementing an authenticated dictionary with improved space bounds in the three-party model—the traditional model of authenticated data structures [47]. Specifically, we can reduce the space of the trusted source from  $O(n)$  to  $O(\log n)$ . This is because in the three-party model of authenticated data structures, the only goal of the trusted source is to update the publish the latest digest  $d_h$ , which, in our construction can be achieved in a streaming fashion (by storing only the previous digest  $d_{h-1}$ ) and without having access to all the elements of the dictionary (only access to the element of the update is required, see Algorithm `updateVerifier`). In previous implementations however (e.g., [24, 40]), the source keeps all the Merkle tree locally (otherwise the digest cannot be updated), therefore requiring  $O(n)$  local space.

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## Appendix

### 7.1 Proof of collision resistance (proof of Theorem 2)

Since generalized hash trees  $(T, \lambda, f, h)$  and  $(T, l, f, h)$  comprise a collision, it is  $\lambda(\epsilon) = l(\epsilon)$  and there exists  $v_\ell \in T$  such that  $\lambda(v_\ell) \neq l(v_\ell)$ —see Definition 11. Consider now the path of nodes  $v_\ell, v_{\ell-1}, \dots, v_1, v_0 = \epsilon$  from node  $v_\ell$  to the root  $v_0 = \epsilon$  of  $T$ . Let also  $w_\ell, w_{\ell-1}, \dots, w_1$  be the siblings of the nodes  $v_\ell, v_{\ell-1}, \dots, v_1$  respectively. We define the following events: **(1)**  $\mathcal{E}_{\ell,0}: l(v_\ell) \neq \lambda(v_\ell)$ ; **(2)**  $\mathcal{E}_{i,0}: l(v_i) \neq \lambda(v_i)$  for  $i = \ell-1, \dots, 1$ ; **(3)**  $\mathcal{E}_{i,0}: l(v_i) = \lambda(v_i)$  for  $i = \ell-1, \dots, 1$ ; **(4)**  $\mathcal{E}_{0,1}: \lambda(\epsilon) = l(\epsilon)$ . The probability that a PPT algorithm can output a collision  $(T, \lambda, f, h)$  and  $(T, l, f, h)$  is at most

$$\begin{aligned} & \Pr[\mathcal{E}_{\ell,0} \cap (\mathcal{E}_{\ell-1,0} \cup \mathcal{E}_{\ell-1,1}) \cap (\mathcal{E}_{\ell-2,0} \cup \mathcal{E}_{\ell-2,1}) \cap \dots \cap \mathcal{E}_{0,1}] \\ & \leq \Pr[\mathcal{E}_{\ell,0} \cap \mathcal{E}_{\ell-1,1}] + \dots + \Pr[\mathcal{E}_{1,0} \cap \mathcal{E}_{0,1}] = \sum_{0 \leq i \leq \ell-1} \Pr[\mathcal{E}_{i+1,0} \cap \mathcal{E}_{i,1}]. \end{aligned}$$

Note that the event  $\mathcal{E}_{i+1,0} \cap \mathcal{E}_{i,1}$  is equivalent to the set of conditions: **(i)**  $l(v_{i+1}) \neq \lambda(v_{i+1})$ ; **(ii)**  $l(v_i) = \lambda(v_i)$  (recall  $v_0 = \epsilon$ ).

It is easy to see that if  $h$  is collision resistant, the probability  $\Pr[\mathcal{E}_{i+1,0} \cap \mathcal{E}_{i,1}]$  is  $\text{neg}(k)$  since it is equivalent with outputting a collision to function  $h$ : Since both  $(T, \lambda, f, h)$  and  $(T, l, f, h)$  are generalized hash trees it is  $f(l(v_i)) = h(l(v_{i+1}), l(w_{i+1}))$  and  $f(\lambda(v_i)) = h(\lambda(v_{i+1}), \lambda(w_{i+1}))$ . Since now  $\lambda(v_i) = l(v_i)$  we have  $f(\lambda(v_i)) = f(l(v_i))$ . But  $l(v_{i+1}) \neq \lambda(v_{i+1})$  and therefore  $(\lambda(v_{i+1}), \lambda(w_{i+1}))$  is a collision with the pair  $(l(v_{i+1}), l(w_{i+1}))$ . Therefore the probability  $\Pr[\mathcal{E}_{i,0} | \mathcal{E}_{i-1,1}]$  is  $\text{neg}(k)$ , implying that the sum  $\sum_{i=0}^{\ell-1} \Pr[\mathcal{E}_{i,0} | \mathcal{E}_{i-1,1}]$  is also  $\text{neg}(k)$ , as  $T$  has polynomial depth and  $\ell$  is no greater than  $T$ 's depth.  $\square$

### 7.2 Proof of security (stated in Theorem 4)

Fix the security parameter  $k$  and output  $\text{pk} = (\mathbf{L}, \mathbf{R}, q, \mathcal{U})$  by calling algorithm `genkey`. Let  $\text{Adv}$  be a PPT adversary. Let  $D_0$  an initial structured binary tree  $T_C$  where  $c_i = 0$  for  $i = 0, \dots, M-1$  and let  $d_0$  be the state output by `initialize`( $D_0, \text{pk}$ ).

1. *Update*. For  $t = 1, \dots, h \leq n$ , the adversary  $\text{Adv}$  picks an element  $x_t \in \mathcal{U}$ . Let  $d_t$  be the final state output by calling `updateVerifier` for every element  $x_t$  and let  $T_C$  be the final structured binary tree  $D_t$  after all the updates have been performed, where  $\mathcal{C} = [c_0, c_1, \dots, c_{M-1}]$ .
2. *Forge*. Let  $x \in \mathcal{U}$  be a query element and  $\alpha \neq c_x$  be an *incorrect* value for index  $x$  picked by  $\text{Adv}$  (as in Definition 3). The adversary  $\text{Adv}$  outputs

$$\Pi(x) = \{(l(v_\ell), l(w_\ell)), (l(v_{\ell-1}), l(w_{\ell-1})), \dots, (l(v_1), l(w_1))\}$$

as the proof for element  $x$ , where  $v_\ell$  is the node corresponding to index  $x$ .

We prove that the probability that  $1 \leftarrow \text{verify}(x, \alpha, \Pi(x), d_h, \text{pk})$  while  $\alpha \neq c_x$  is negligible. To do that we consider the full binary tree  $T$  (see Definition 8) defined by the nodes  $(v_\ell, w_\ell), (v_{\ell-1}, w_{\ell-1}), \dots, (v_1, w_1)$  and the root node  $\epsilon$ . It is easy to see that since the verification algorithm accepts,  $(T, l, f, h_n)$  is a generalized hash tree as defined in Definition 10, and where  $l$  is the labeling in  $\Pi(x)$ .

Consider now the structured binary tree  $T_{\mathcal{C}}$  (where  $\mathcal{C} = [c_0, c_1, \dots, c_{M-1}]$ ) as defined in Definition 12. Let  $T'_{\mathcal{C}}$  be the subtree of  $T_{\mathcal{C}}$  that has the same nodes as  $T$ . By Theorem 3, the adversary can compute  $(T, \lambda, f, h_n)$ , which is also a generalized hash tree. However, since  $\alpha \neq c_x$  this means that  $l(v_\ell) \neq \lambda(v_\ell)$ . Note now that  $\lambda(\epsilon) = l(\epsilon) = d_h$  and therefore the adversary has output a tree collision, which, by Theorem 2, happens with probability  $\text{neg}(k)$  since  $h_n$  is collision resistant (see Theorem 1). A same argument applies for the range search query. This completes the proof.  $\square$

### 7.3 Proof of asymptotic performance (stated in Theorem 4)

Algorithm `updateVerifier` requires computing the partial label  $\mathcal{L}_\epsilon(x)$ , where  $x$  is the element of the update and  $\epsilon$  is the root of  $T_{\mathcal{C}}$ . Computing  $\mathcal{L}_\epsilon(x)$  can be achieved in  $O(\log M \log^2 n)$  time, by Lemma 3.

Algorithm `updateProver` needs to compute the partial labels  $\mathcal{L}_{v_j}(x)$  ( $j = \ell, \dots, 0$ ), where  $v_\ell, v_{\ell-1}, \dots, v_0$  are the nodes of the structured binary tree from element  $x$  to the root of the tree. By Definition 15, all these labels can be computed in  $O(\log M \log^2 n)$  time during the computation of  $\mathcal{L}_\epsilon(x)$  (i.e., in one pass). However, to update a label  $\lambda(v_i)$ , one needs to retrieve it from the underlying data structure that stores the “useful” portion of the generalized hash tree (and either store it back or delete if the label becomes  $\mathbf{0}$ ). Therefore `updateProver` needs to spend an extra  $O(\log \nu)$  time in the worst case, where  $\nu$  is the number of the currently stored elements. Since however  $\nu \leq n$ , it follows that the time required is  $O(\log M \log^2 n)$ .

Algorithm `query` for membership and successor queries needs to retrieve  $O(\log M)$  binary representations of  $O(\log n)$  bits each, spending  $O(\log \nu)$  time to retrieve each one of them. Since  $\nu \leq n$ , it follows that query runs in  $O(\log M \log n)$  time. The proof has also size  $O(\log M \log n)$ , since it contains  $O(\log M)$  binary representations of  $O(\log n)$  bits each. Since range search is implemented via  $s$  successor queries, the same bounds apply multiplied with  $s$ , where  $s$  is the size of the output range.

Finally, for the space at the client, it is required that the client store  $d_h$ , which consists of  $k$   $O(\log n)$ -bit numbers, therefore the space at the client is  $O(\log n)$ . For the space at the prover, we recall that we only store labels  $\lambda(v)$  that lie on tree paths starting from leaves  $x$  such that  $c_x > 0$  (all these labels are also non-zero and have  $O(\log n)$  bits). Since at every point in time there are  $\nu$  elements stored in the data structure, it follows that the space at the server is  $O(\nu \log M \log n)$ .